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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10084943	FILING DATE 03/01/2002	CLASS 438	SUBCLASS	GAIL 2812	EXAMINER
**APPLICANTS: Kawakami Kazuya; Suzuki Yukihiro; Okutani Ken; Kajita Susumu Hashimoto Takeshi;					
**CONTINUING DATA VERIFIED:					
**FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-57974 03/02/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials		843.41231X00			
TITLE : Method of manufacturing semiconductor integrated circuit device and semiconductor manufacturing apparatus					

U.S. DEPT. OF COMM. / PAT. & TM-PTO-436L (Rev. 12-94)

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims Print Claim for O.G.	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner PREPARED FOR ISSUE Application Examiner	
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